

PATENT  
790001-2034**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**RECEIVED  
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JUN 22 2005

*Entered  
Per RCE  
7/5/05*

Applicants : MATSUNAGA et al.  
Serial No. : 10/628,689  
Filing Date : 28 July 2003  
For : SEMICONDUCTOR DEVICE AND ITS MANUFACTURING  
METHOD  
Examiner : Hoai V. Pham  
Art Unit : 2814

745 Fifth Avenue  
New York, NY 10151**FACSIMILE**I hereby certify that this paper is being facsimile transmitted to the  
Patent and Trademark Office on the date shown below.

Howard C. Lee, Reg. No. 48,104

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Signature

June 22, 2005

Date of Signature

**EXPEDITED PROCEDURE  
RESPONSE AFTER FINAL  
ACTION UNDER 37 CFR 1.116****AMENDMENT UNDER 37 C.F.R. § 1.116  
AND REQUEST FOR INTERVIEW**

Mail Stop: AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This is in response to the final Office Action dated 22 March 2005, setting a three month term for reply. As this response is being filed before the end of the term for reply, it is believed that no fees are due. However, the Commission is authorized to charge any fee occasioned by this paper, or credit any overpayment of such fees, to Deposit Account No. 50-0320.